

IN THE SPECIFICATION:

Please amend the paragraph beginning at page 1, line 5, as follows.

The present invention is related to United States Patent Application Number 09/785,604, entitled "Method and Apparatus for Distributing a Self-Synchronized Clock to Nodes on a Chip," ~~(Attorney Docket Number Lee-14-5-3)~~, United States Patent Application Number 09/785,602, entitled "Method and Apparatus for Distributing Multi-Source/Multi-Sink Control Signals Among Nodes on a Chip," ~~(Attorney Docket Number Fernando-10-12-5)~~, United States Patent Application Number 09/758,653, entitled "Bidirectional Bus Repeater for Communications on a Chip," ~~(Attorney Docket Number Hunter-4-13-4)~~ and United States Patent Application Number 09/758,592, entitled "On-Chip Method and Apparatus for Transmission of Multiple Bits Using Quantized Voltage Levels," ~~(Attorney Docket Number Lee-15-6)~~, each filed contemporaneously herewith, assigned to the assignee of the present invention and incorporated by reference herein.